

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.	:	10/598524	Confirmation No.	6872
Applicant	:	Katsuya Fukase et al.		
Filed	:	September 1, 2006		
TC/A.U.	:	N/A		
Examiner	:	N/A		
 Title	:	 CIRCUIT BOARD MANUFACTURING METHOD AND CIRCUIT BOARD		
Docket No.	:	NGB-16837		
Customer No.	:	040854		

LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Enclosed herewith is an English translation of the International Preliminary Examination Report for filing in the above-identified application.

Respectfully submitted,

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